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Attorney Docket No. 29083/39328
PATENT

JUL 31 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Mail Stop Provisional Patent Application
Commissioner for Patents
P.O. Box 1450
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Sir:

This is a request for filing a PROVISIONAL APPLICATION under 37 CFR 1.153(b)(2).

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For: RAKE RECEIVER WITH MULTI-PATH INTERFERENCE ACCOMMODATION

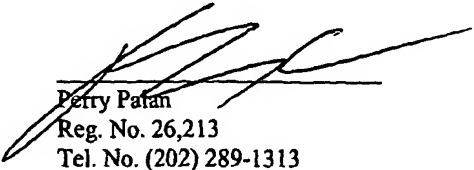
Enclosed are:

The Specification, including claims and abstract (8 pages); and
1 sheet of 1 informal drawing.

APPLICANT IS A SMALL ENTITY.

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Attorney Docket No. 29083/39328
PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

SPECIFICATION

INVENTION:

RAKE RECEIVER WITH MULTI-PATH INTERFERENCE ACCOMMODATION

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BACKGROUND AND SUMMARY

[0001] The present disclosure is directed to multi-path cancellation in wireless communication and more specifically to accommodating for the multi-path interference.

[0002] Multi-path interference is an undesired phenomenon characteristic in the wireless communication field. In certain cases, it can produce serious communication channel impairment. It is characteristic to dense urban areas and confined areas such as shopping malls. One form of multi-path interference cancellation (MPIC) is suggested in "Channel Tracking for Rake Receivers in Closely Spaced Multipath Environments," Fock et al., IEEE Journal on Selected Areas in Communications, Vol. 19, No. 12, pp. 2420-31, December 2001. It uses fading coefficients and relative delays to compute a compensation term that allows cancellation of the effect of other paths on the timing error.

[0003] The present method extracts data from a received signal including multi-path interference in a rake receiver. The method includes sampling and filtering the received signal; estimating a time delay $\hat{\tau}_l$ between paths for the filtered samples $\psi(\tau)$; and estimating channel complex coefficient \hat{c}_l for the filtered samples $\psi(\tau)$. Transmitted data $\mathbf{x}(\tau_l)$ is extracted from the filtered samples $\psi(\tau)$ for each path l by solutions of simultaneous equations of the following filtered samples $\psi(\tau)$ equation

$$\psi(\tau)\mathbf{R}_{ff}^{-1}(\tau_k - \hat{\tau}_0)\Lambda_{ss}^H(\hat{\tau}_k) = \sum_{l=0}^{N_p-1} c_l(\tau_l)\mathbf{x}(\tau_l)\Lambda_{ss}(\tau_l)\mathbf{R}_{ff}(\tau_l - \hat{\tau}_0)\mathbf{R}_{ff}^{-1}(\tau_k - \hat{\tau}_0)\Lambda_{ss}^H(\hat{\tau}_k) + \vec{n}(\tau)$$

wherein k is a particular path, N_p is the number of visible paths, $\mathbf{R}_{ff}(\tau_l - \hat{\tau}_0)$ is a double convolution matrix of the filtering process and $\mathbf{R}_{ff}^{-1}(\tau_k - \hat{\tau}_0)$ is the pseudo inverse, $\Lambda_{ss}(\tau_l)$ is the product of spreading and scrambling matrices and $\Lambda_{ss}^H(\hat{\tau}_k)$ is the inverse, and $\vec{n}(\tau)$ is noise.

[0004] The method is selectively performed for estimated time delay $\hat{\tau}_l$ of a duration of one chip or less of the receiver. If the time delay is greater than one

chip, no extraction is necessary. The filtered samples $\psi(\tau)$ equations without the noises $n(\tau)$ are solved. The number of solutions may be less or equal to the number of over-samples per chip of the receiver. The solutions of the simultaneous equations may be stored in the receiver and the method performed in software.

[0005] These and other aspects of the present invention will become apparent from the following detailed description, when considered in conjunction with accompanying drawings.

BRIEF DESCRIPTION OF DRAWINGS

[0006] Figure 1 is a block diagram of a rake receiver.

DETAILED DESCRIPTION

[0007] Figure 1 illustrates a generic representation of a rake receiver. The elements are well known and not described herein in detail. Only those elements or functional portions which are used in the present method are illustrated. The rake receiver 10 includes a rake searcher 12 and a channel estimator 14 providing path delay estimations $\tau_1, \tau_2 \dots \tau_3$ and estimates to the channel coefficients $c_1, c_2 \dots c_3$ to the main processing portion 16. The main processing portion 16 includes a multi-path interference cancellation (MPIC) portion and a Maximum Ratio Combiner (MRC) portion. Depending upon the structure, these may be hardware or software routines. As described herein, the MPIC is performed in software and achieves multi-path interference cancellation or accommodation by representing the received filtered signal with a linear equation and extracting the transmitted data from the filtered sample for each path by a solution of simultaneous equations of the filtered sample equation.

[0008] The present disclosure does not depend upon what method is used to estimate the path delays nor the channel coefficients. Any method of the prior art may be used, as well as any method that may be developed in the future.

[0009] The linear equation to be solved is as follows:

$$\begin{aligned}
 \psi(\tau) \mathbf{R}_f^{-1}(\tau_k - \hat{\tau}_0) \Lambda_{ss}^H(\hat{\tau}_k) &= \sum_{l=0}^{N_p-1} c_l(\tau_l) \mathbf{x}(\tau_l) \Lambda_{ss}(\tau_l) \mathbf{R}_f(\tau_l - \hat{\tau}_0) \mathbf{R}_f^{-1}(\tau_k - \hat{\tau}_0) \Lambda_{ss}^H(\hat{\tau}_k) + \bar{\mathbf{n}}(\tau) \mathbf{R}_f^{-1}(\tau_k - \hat{\tau}_0) \Lambda_{ss}^H(\hat{\tau}_k) \\
 &= x(\hat{\tau}_k) c_k(\tau_k) \Lambda_{ss}(\tau_k) \Lambda_{ss}^H(\hat{\tau}_k) + \\
 &\quad + \sum_{\substack{l=0 \\ l \neq k}}^{N_p-1} c_l(\tau_l) \mathbf{x}(\tau_l) \Lambda_{ss}(\tau_l) \mathbf{R}_f(\tau_l - \hat{\tau}_0) \mathbf{R}_f^{-1}(\tau_k - \hat{\tau}_0) \Lambda_{ss}^H(\hat{\tau}_k) + \bar{\mathbf{n}}(\tau)
 \end{aligned} \tag{1}$$

where: estimating channel complex coefficients $c_l(\tau_l)$ is assumed constant for the duration of a data block;

$\psi(\tau)$ is filtered samples;

$\hat{\tau}_l$ is the time delay between paths for the filtered samples $\psi(\tau)$;

$\mathbf{x}(\tau_l)$ is transmitted data to be extracted;

$\mathbf{R}_f(\tau_l - \hat{\tau}_0)$ is a double convolution matrix of the filtering process, and $\mathbf{R}_f^{-1}(\tau_k - \hat{\tau}_0)$ is the pseudo inverse;

k is a particular path;

N_p is the number of visible paths;

$\Lambda_{ss}(\tau_l)$ is the product of spreading s and scrambling S matrices and $\Lambda_{ss}^H(\hat{\tau}_k)$ is the inverse; and

$\bar{\mathbf{n}}(\tau)$ is noise and $\bar{\mathbf{n}}(\tau) \mathbf{R}_f^{-1}(\tau_0 - \hat{\tau}_0) \Lambda_{ss}^H(\hat{\tau}_0) = \bar{\mathbf{n}}(\tau)$

[00010] The last sum in the right hand side of equation (1):

$$\sum_{l=1}^{N_p-1} c_l(\tau_l) \mathbf{x}(\tau_l) \Lambda_{ss}(\tau_l) \mathbf{R}_f(\tau_l - \hat{\tau}_0) \mathbf{R}_f^{-1}(\tau_k - \hat{\tau}_0) \Lambda_{ss}^H(\hat{\tau}_k) \tag{2}$$

is the multi-path interference.

[00011] If the path delay is larger than a chip, the last sum in equation (1) is negligible due to the correlation properties of the scrambling matrix S . Equation (1) becomes

$$\psi(\tau) \mathbf{R}_f^{-1}(\tau_k - \hat{\tau}_0) \Lambda_{ss}^H(\hat{\tau}_k) = x(\hat{\tau}_k) c_k(\tau_k) \Lambda_{ss}(\tau_k) \Lambda_{ss}^H(\hat{\tau}_k) \tag{3}$$

wherein the solution for transmitted data $\mathbf{x}(\tau_l)$ is

$$\mathbf{x}(\hat{\tau}_k) = \frac{\psi(\tau) \mathbf{R}_f^{-1}(\tau_k - \hat{\tau}_0) \Lambda_{ss}^H(\hat{\tau}_0) \hat{c}_k^*(\tau_k)}{\alpha M} \tag{4}$$

wherein where $\alpha = c_0(\tau_0) \hat{c}_0^*(\tau_0)$ is constant for the duration of a data block, and $M = \Lambda_{ss}(\tau_k) \Lambda_{ss}^H(\tau_k)$.

[00012] If equation(4) is used for path delays larger than a chip, it represents an equalizer. Thus, for each path there will be a simple solution. All x_k are summed up in a certain ratios using coefficients $\hat{\beta}_l$ in the MRC. If the entries for $R_{ff}^{-1}(\tau_k - \tau_0) \Lambda_{ss}^H(\tau_0) \hat{c}_k^*(\tau_k)$ are stored in the memory, equation (4) is executed in software.

[00013] For path delays less than a chip, the last term is not negligible; therefore, a better symbol estimate can be obtained by extracting the data from the received signal, including the multi-path interference term, by solving all the simultaneous equations for all of the paths using equation (1).

[00014] The final estimate is the sum of the estimates using coefficients $\hat{\beta}_l$:

$$\hat{x} = \sum_{l=0}^{N_p-1} \hat{\beta}_l x(\tau_l) \quad (5)$$

[00015] The following is an example for two path delays less than a chip apart. In order to simplify the writing, the following notation will be used:

$$R_{ff}(\tau_l - \tau_0) = R(l), R_{ff}^{-1}(\tau_k - \tau_0) = R^{-1}(k), \Lambda(\tau_l) = \Lambda(l), \Lambda(\tau_k) = \Lambda(k), c_l(\tau_l) = c(l)$$

and $x(\tau_k) = x(k)$. Equation (1) for each of the path delays becomes:

$$\begin{cases} \Psi(\tau) R^{-1}(0) \Lambda^H(0) = c_0 x(0) \Lambda(0) R(0) R^{-1}(0) \Lambda^H(0) + c_1 x(1) \Lambda(1) R(1) R^{-1}(0) \Lambda^H(0) + n_1 \\ \Psi(\tau) R^{-1}(1) \Lambda^H(1) = c_0 x(0) \Lambda(0) R(0) R^{-1}(1) \Lambda^H(1) + c_1 x(1) \Lambda(1) R(1) R^{-1}(1) \Lambda^H(1) + n_2 \end{cases} \quad (6)$$

Ignoring the noise n , the two simultaneous equations (6) are solved with the following solutions:

$$\begin{aligned} x(0) &= \frac{1}{c_0} \Psi(\tau) \frac{R^{-1}(0) \Lambda^H(0) \Lambda(1) \Lambda^H(1) - R^{-1}(1) \Lambda(1) \Lambda^H(1) \Lambda(0) R(0) R^{-1}(0) \Lambda^H(0)}{\Lambda(0) \Lambda^H(0) \Lambda(1) \Lambda^H(1) - \Lambda(0) R(0) R^{-1}(1) \Lambda^H(1) \Lambda(1) R(1) R^{-1}(0) \Lambda^H(0)} \\ x(1) &= \frac{1}{c_1} \frac{\Lambda(0) R(0) R^{-1}(0) \Lambda^H(0) \Psi(\tau) R^{-1}(1) \Lambda^H(1) - \Lambda(0) R(0) R^{-1}(1) \Lambda(1) \Lambda^H(1) \Psi(\tau) R^{-1}(0) \Lambda^H(0)}{\Lambda(0) \Lambda^H(0) \Lambda(1) \Lambda^H(1) - \Lambda(0) R(0) R^{-1}(1) \Lambda^H(1) \Lambda(1) R(1) R^{-1}(0) \Lambda^H(0)} \end{aligned} \quad (7)$$

[00016] Theoretically, the number of visible paths will be less than or equal to the number of over-samples N_{OVS} . Therefore, the maximum number of

$\mathbf{R}_{ff}^{-1}(\tau_k - \hat{\tau}_0)$ matrixes will be N_{OVS} , where the values $\tau_l - \hat{\tau}_0 = \Delta\tau$ are equal to the sampling rate. All matrixes in the above expression can be pre-calculated and stored in the memory. This may depend on the communication format and memory capacity.

[00017] Although this application has been designed for 3G WCDMA, it is applicable for other types of networks or communication protocols.

[00018] Although the present disclosure has been described and illustrated in detail, it is to be clearly understood that this is done by way of illustration and example only and is not to be taken by way of limitation. The scope of the present invention is to be limited only by the terms of the appended claims.

WHAT IS CLAIMED:

1. A method of extracting data from a received signal including multi-path interference in a rake receiver, comprising:
 sampling and filtering the received signal;
 estimating a time delay $\hat{\tau}_l$ between paths for the filtered samples $\psi(\tau)$;
 estimating channel complex coefficient \hat{c}_l for the filtered samples $\psi(\tau)$;
 extracting transmitted data $\mathbf{x}(\tau_l)$ from the filtered samples $\psi(\tau)$ for each path l
 by solutions of simultaneous equations of the following filtered samples $\psi(\tau)$
 equation

$$\psi(\tau) \mathbf{R}_f^{-1}(\tau_k - \hat{\tau}_0) \Lambda_{ss}^H(\hat{\tau}_k) = \sum_{l=0}^{N_p-1} c_l(\tau_l) \mathbf{x}(\tau_l) \Lambda_{ss}(\tau_l) \mathbf{R}_f(\tau_l - \hat{\tau}_0) \mathbf{R}_f^{-1}(\tau_k - \hat{\tau}_0) \Lambda_{ss}^H(\hat{\tau}_k) + \tilde{\mathbf{n}}(\tau)$$

wherein k is a particular path, N_p the number of visible paths, $\mathbf{R}_f(\tau_l - \hat{\tau}_0)$ is a double convolution matrix of the filtering process and $\mathbf{R}_f^{-1}(\tau_0 - \hat{\tau}_0)$ is the pseudo inverse, $\Lambda_{ss}(\tau_l)$ is the product of spreading and scrambling matrices and $\Lambda_{ss}^H(\hat{\tau}_0)$ is the inverse, and $\tilde{\mathbf{n}}(\tau)$ is noise.

2. The method according to Claim 1, wherein the method is selectively performed for estimated time delay $\hat{\tau}_l$ of a duration of one chip or less of the receiver.
3. The method according to Claim 1, wherein the filtered samples $\psi(\tau)$ equations without the noises $\tilde{\mathbf{n}}(\tau)$ are solved.
4. The method according to Claim 1, wherein the number of solutions is less than or equal to the number of over-samples per chip of the receiver.
5. The method according to Claim 1, wherein the solutions of the simultaneous equations are stored in the receiver and the method is performed in software.

6. The method according to Claim 5, wherein the number of stored solutions is less than or equal to the number of over-samples per chip of the receiver.

ABSTRACT OF THE DISCLOSURE

A method of extracting data from a received signal including multi-path interference in a rake receiver. The method includes sampling and filtering the received signal; estimating a time delay $\hat{\tau}_l$ between paths for the filtered samples $\psi(\tau)$; and estimating channel complex coefficient \hat{c}_l for the filtered samples $\psi(\tau)$. Transmitted data $x(\tau_l)$ is extracted from the filtered samples $\psi(\tau)$ for each path l by solutions of simultaneous equations of the following filtered samples $\psi(\tau)$ equation

$$\psi(\tau) \mathbf{R}_{ff}^{-1}(\tau_k - \hat{\tau}_0) \mathbf{\Lambda}_{ss}^H(\hat{\tau}_k) = \sum_{l=0}^{N_p-1} c_l(\tau_l) x(\tau_l) \mathbf{\Lambda}_{ss}(\tau_l) \mathbf{R}_{ff}(\tau_l - \hat{\tau}_0) \mathbf{R}_{ff}^{-1}(\tau_k - \hat{\tau}_0) \mathbf{\Lambda}_{ss}^H(\hat{\tau}_k) + \vec{n}(\tau)$$

wherein k is a particular path, N_p is the number of visible paths, $\mathbf{R}_{ff}(\tau_l - \hat{\tau}_0)$ is a double convolution matrix of the filtering process and $\mathbf{R}_{ff}^{-1}(\tau_k - \hat{\tau}_0)$ is the pseudo inverse, $\mathbf{\Lambda}_{ss}(\tau_l)$ is the product of spreading and scrambling matrices and $\mathbf{\Lambda}_{ss}^H(\hat{\tau}_k)$ is the inverse, and $\vec{n}(\tau)$ is noise.

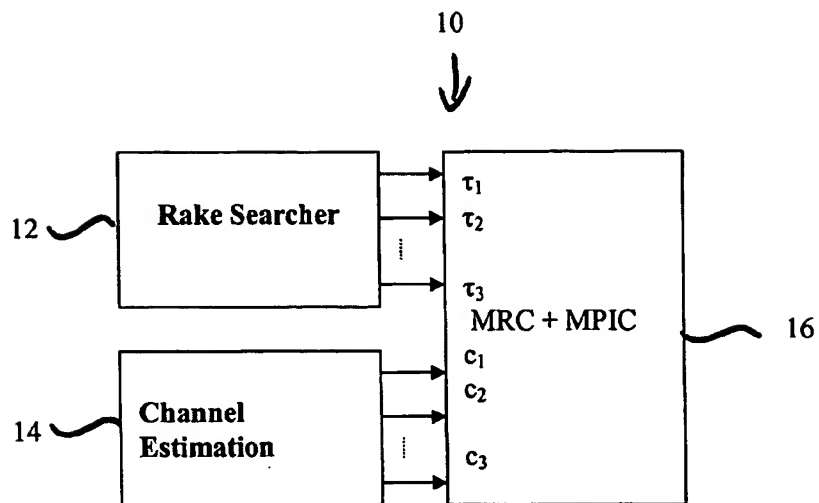


Figure 1